

Enhancement of the power factor in two-phase silicon-boron nanocrystalline alloys

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In previous publications it was shown that the precipitation of silicon boride around grain boundaries may lead to an increase of the power factor in nanocrystalline silicon. Such an effect was further explained by computational analyses showing that the formation of an interphase at the grain boundaries along with high boron densities can actually lead to a concurrent increase of the electrical conductivity σ and of the Seebeck coefficient S . In this communication we report recent evidence of the key elements ruling such an unexpected effect. Nanocrystalline silicon films deposited onto a variety of substrates were doped to nominal boron densities in excess of 10^{20} cm⁻³ and were annealed up to 1000 °C to promote boride precipitation. Thermoelectric properties were measured and compared with their microstructure. A concurrent increase of σ and S with the carrier density was found only upon formation of an interphase. Its dependency on the film microstructure and on the deposition and processing conditions will be discussed.

I. INTRODUCTION

The rush for high-efficiency thermoelectric materials has undergone a sudden acceleration over the last decade. On one side, efficiency could be improved in dimensionally limited systems such as nanowires, that display lowered thermal conductivities κ while preserving their electrical conductivities σ and their Seebeck coefficients S ¹⁻³. On the other side, enhancements of the power factor $P \equiv S^2\sigma$ was shown to occur in some systems by straining⁴, energy filtering⁵, δ -doping^{6,7}, and quantum confinement⁸. All approaches lead to increased figures of merit $Z \equiv P/\kappa$, although material usability in practical contexts may quite differ whether high Z values are due to low thermal conductivities or to high power factors⁹.

In previous publications¹⁰⁻¹² we reported that annealing polycrystalline silicon doped with boron by ion implantation at total final densities in excess to boron solid solubility leads to an unexpected simultaneous increase of both the electrical conductivity and of the Seebeck coefficient. Such a finding, strikingly counterintuitive for diffusive charge transport, was shown^{13,14} to be possible in a composite, two-phase system if (a) carriers are energy filtered at potential barriers, (b) charge transport involves high energy carriers, and (c) the phase showing the largest Seebeck coefficient is also characterised by the lowest thermal conductivity. Thus, provided that processing of heavily doped silicon leads to the formation of a silicon-silicon boride two-phase system, where silicon boride precipitates have sizes of a few nanometers and silicon grain size is comparable to the majority carrier energy-relaxation mean free path.

This paper reports the first experimental validation of

the model presented in Refs.^{13,14}. A variety of silicon-based systems meeting only some of the model requirements were prepared, changing dopant concentration and material micromorphology (i.e. grain size and orientation). Micromorphology was modified by changing the substrate and the deposition conditions. Substrates were chosen so that their different thermal resistances might promote the growth of films with different grain sizes and shapes. This allowed to verify whether the anomalous increase of the power factor actually requests all the ingredients the model predicts as needed. It will be shown that the anomalous growth of the power factor at the highest annealing temperatures T_a actually requires the concurrent presence of a nanograined morphology along with the formation of boron-enriched nanoprecipitates.

II. EXPERIMENTAL

Table I summarizes the characteristics of all samples studied in this paper. In short, three types of substrates were used, namely oxidised silicon (S family), a SiO_x-Si₃N₄ bilayer deposited onto oxidised silicon (T family), and a silica substrate (Q sample). Polycrystalline films were all deposited by chemical vapour deposition (CVD). Silane was used, always keeping the substrate at 610 °C but for sample S1, for which a deposition temperature of 600 °C was chosen. In addition, silicon-on-insulator (SOI) single crystalline films were also analysed. All samples were boron doped by ion implantation at different doses and energies to obtain degenerate silicon films with nominal boron densities ranging around its solubility threshold at 500 °C¹⁵. After damage recovery, sam-

ID	Thickness (nm)	Ion implantation energy (keV)	Dose (cm^{-2})	Total boron density (cm^{-3})	Damage recovery conditions	Grain size (nm)	Second phase
S1	450	60	2.0×10^{16}	4.4×10^{20}	1050 °C, 30 s	100 \perp , 30 – 50	observed
S2	200	30	0.4×10^{16}	2.0×10^{20}	1100 °C, 300 s	100 \perp , 30 – 50	absent
S3	200	30	0.8×10^{16}	4.0×10^{20}	1100 °C, 300 s	100 \perp , 30 – 50	absent
S4	200	30	1.2×10^{16}	6.0×10^{20}	1100 °C, 300 s	80 – 100 \perp , 50 – 100	observed
T1	200	30	1.2×10^{16}	6.0×10^{20}	1100 °C, 300 s	80 \perp , 50 – 100	absent
T2	200	30	0.4×10^{16}	2.0×10^{20}	1100 °C, 300 s		
Q1	490	40	2.5×10^{16}	5.1×10^{20}	1100 °C, 300 s	100 \perp , 100 – 150	observed
SOI	342	50	2.5×10^{16}	3.5×10^{20}	1100 °C, 300 s	–	

TABLE I: Summary of the samples analysed in this paper. Families S, T, and Q refer respectively to thin films deposited onto oxidised silicon, a Si_3N_4 layer deposited onto oxidised silicon, and vitreous silica. Symbols || and \perp label directions referred to the substrate.

ples underwent sequential thermal treatments up to 1000 °C in argon, each step lasting two hours. Al–Si 5% pads were evaporated for all transport measurements and were removed by HCl followed by piranha etch (H_2O_2 33 % vol. + H_2SO_4 98 % vol. in a 1:2 ratio, 95 °C, 30 minutes) prior to the subsequent annealing.

Electrical conductivity, Hall effect, and Seebeck coefficient measurements were carried out. Conductivity was determined by current-voltage characteristics at 20 °C. Measurements of σ as a function of the temperature confirmed for all samples a negative temperature coefficient as expected in a degenerate semiconductor. Samples for the Seebeck and the electrical conductivity measurements were obtained by cutting $50 \times 5 \text{ mm}^2$ rectangular chips and evaporating metal contacts through a shadow mask. For Hall measurements, $17 \times 17 \text{ mm}^2$ samples were cut and metal contacts were evaporated on small areas in the four corners according to the Van der Pauw geometry. Hall measurements were carried out at room temperature with a maximum magnetic field of 0.5 T. Seebeck coefficient was measured using a lab-made apparatus that was calibrated toward single-crystal silicon samples of known doping level. The apparatus allowed for temperature differences up to 80 °C. Each set of measurements was repeated on the same sample at least three times to ensure data reliability. Further details concerning the experimental setup are reported elsewhere¹⁰.

Cross-sections for electron microscopy (EM) analyses were prepared by conventional methods, grinding, polishing and finally thinning the samples with an argon ion beam. EM analyses were performed both in low-energy (30 keV) dark field scanning transmission (DF-STEM) and in high-energy (200 keV) transmission (TEM) modes. Low-energy analyses were performed with a FEI Strata235M equipped with bright field –dark field solid state detector. TEM and energy filtered electron spectroscopic images (ESI) were obtained using a JEM2011 electron microscope (spherical aberration coefficient 0.5 mm, chromatic aberration coefficient 1.1 mm) equipped with conventional LaB_6 electron source and an electron energy loss imaging filter (GIF 200[®]). This at-

tachment allows to record both electron energy loss spectroscopy data and ESI.

III. RESULTS

As mentioned, annealing of sample S1 led to an unexpected increase of the power factor as a result of the concurrent increase of its electrical conductivity and of the Seebeck coefficient. Hall effect measurements showed that annealing at temperatures higher than 800 °C resulted in a decrease of the majority carrier density p due to the precipitation of metastable substitutional boron. This causes the formation of potential barriers and obviously leads to an increase of S . The unexpected finding was that also the hole drift mobility μ_D steeply increased, overcompensating the detrimental effect of the lower carrier density on σ . Fig. 1 summarizes the experimental dependency of both μ_D and S on p . TEM analyses (Fig. 2) showed that, while the thermal processing did not significantly modify the grain size, it caused a precipitation of a boron-rich second phase *around* the grain boundaries — not decorating the grain boundaries themselves. Such a unique feature is known and reported in literature for boron¹⁶, and encouraged to correlate the anomalous dependency of the mobility to the formation of potential barriers associated to the presence of precipitates. All polycrystalline films exhibited columnar growth, with grain aspect ratio somewhat larger in the S than in the T or Q families.

Figure 3 reports a comparison of the span of S , σ , and of the power factor measured on the samples considered in this work and consequent to the thermal processing.

IV. DISCUSSION

Of the three features considered as possibly responsible for the increase of the power factor, the role of degeneracy in itself can be immediately discarded. Single-crystalline SOI film, although degenerate, did not display any incre-

ment of the power factor significantly above the value theoretically expected for silicon ($\approx 3 \text{ mW K}^{-2} \text{ m}^{-117}$). This is in good agreement with a rather large body of previous studies on heavily doped silicon^{17–19}. Not even the precipitation of a second phase, expected for a heat-treated material with a nominal boron density $> 2 \times 10^{20} \text{ cm}^{-3}$ appears to be sufficient to determine the mobility enhancement observed in S1.

Moving to polycrystalline films, the role played by the total boron concentration is quite evident comparing samples from the S and T families. Whenever boron content is lower than $4 \times 10^{20} \text{ cm}^{-3}$, no precipitation of boron is either expected or observed — and no anomalous increase either of the mobility or of the power factor is found. It may be worthwhile to stress that the actual formation of boron precipitates is not simply related to thermodynamic considerations. Apart from the effect on the solubility threshold of grain boundaries and their eventual decoration²⁰, the formation of a second phase upon relatively short (2 hours) heat treatments is also ruled by boron diffusivity. Using standard models²¹ it can be verified²² that, in the range of temperature–time conditions considered in this study, boride precipitation occurs only for initial boron concentrations exceeding $\approx 4 \times 10^{20} \text{ cm}^{-3}$. This is consistent with our experimental results, where no formation of boron-rich second phase was actually observed for lower initial boron densities.

Boron supersaturation is not however sufficient to induce a power factor enhancement. While actually sample S4 reports a power factor value comparable to that observed on S1, no likely effect is observed either on sample T1 or Q1. Interestingly enough, sample Q1 displays a concurrent increase of S and σ upon annealing, but the final power factor value it reaches after the heat treatment at $1000 \text{ }^\circ\text{C}$ is about one order of magnitude lower than that observed in S1. A key to understand such a difference is in the grain size, much larger for Q1 than for S1. Since, according to the model, energy filtering is

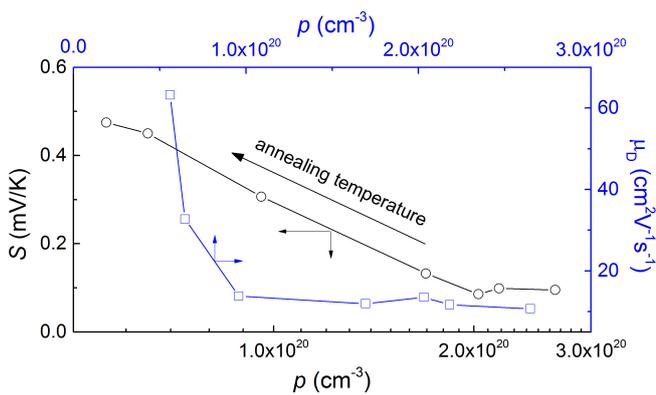


FIG. 1: Experimental dependency of drift mobility and Seebeck coefficient on the hole density for sample S1. Carrier density was modulated by sequential annealing at increasing temperatures that promotes a gradual precipitation of boron.

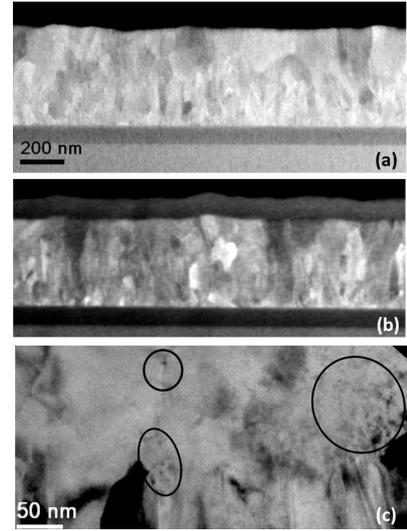


FIG. 2: (a) DF-STEM image of the as-deposited S1 sample; (b) DF-STEM and (c) TEM bright field image of the same sample, both taken after annealing at $1000 \text{ }^\circ\text{C}$. Circled areas in the TEM image mark diffraction contrast details due to the precipitation of a second phase. Note the preservation of grain sizes upon annealing at high temperature.

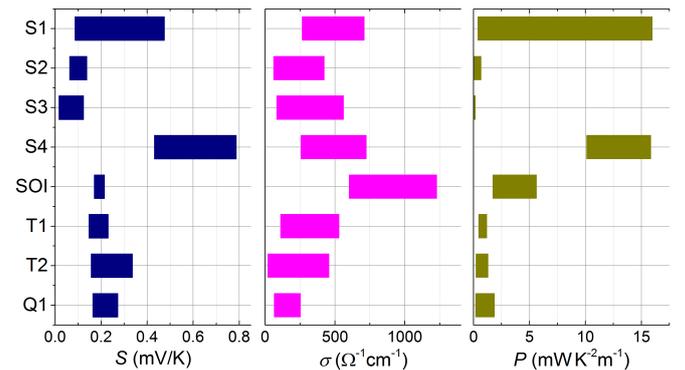


FIG. 3: Variation of the thermoelectric coefficient, the electrical conductivity, and the power factor as a result of thermal processing up to $1000 \text{ }^\circ\text{C}$ on samples described in Table I.

proportional to the volume of grain boundaries and its effect is larger when energy relaxation within the grains is less efficient (i.e. when grain size is small), it is not unreasonable to conclude that carrier filtering is active but less effective in Q1, explaining its lower performances.

Sample T1 is more puzzling. Absence of a second phase clearly explain the lack of power factor enhancement. Yet it is not immediate to understand why, in spite of the high nominal boron density, no precipitation occurs upon annealing. In principle, since diffusivity in polycrystalline materials strongly depends on the structure of grain boundaries, a larger abundance of grains oriented along $[311]$ (reported by TEM) might provide a rationale to the observed metastability of the boron–silicon

solution. Nonetheless, further investigations are surely needed to obtain a fully convincing explanation for the absence of precipitates.

V. SUMMARY AND CONCLUSIONS

To the best of our knowledge, this paper is the first systematic investigation pointing out an enhancement of the power factor in p-type heavily doped polycrystalline silicon due to carrier energy filtering. Based upon the comparison of eight samples differing by micromorphology and boron concentration it seems sensible to conclude that an enhancement of the power factor in silicon does require the concurrency of (a) a grain size enabling only partially energy-relaxed carrier transport, high boron doping enabling high velocity carriers to participate in transport; and (b) the presence of suitable energy filtering barriers preventing slow carriers from participating in the energy and charge transport. Grain sizes from 30 to 50 nm (cf. samples S1 and S4) were found to best

suit such requirements. These results apparently corroborate the transport model previously proposed. Energy filtering in single crystals and in relatively large-grained polycrystalline materials only partially compensates the reduction of σ resulting from a decrease of the actual carrier density. No simultaneous increase of the thermopower and of the electrical conductivity is found as charge transport occurs in the diffusion regime, so that carrier relaxation dumps the effect of energy filtering on μ_D . Instead, in nanocrystalline systems with grains small enough to allow only partial carrier energy relaxation, filtering fully enables the participation of fast carriers only to conduction, leading to the observed overcompensation of the lowered carrier density.

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